



KEKB Control System

**for EPICS Collaboration meeting
Nov. 2000
presented by Noboru Yamamoto**

What's in this talk

▶ KEKB Status

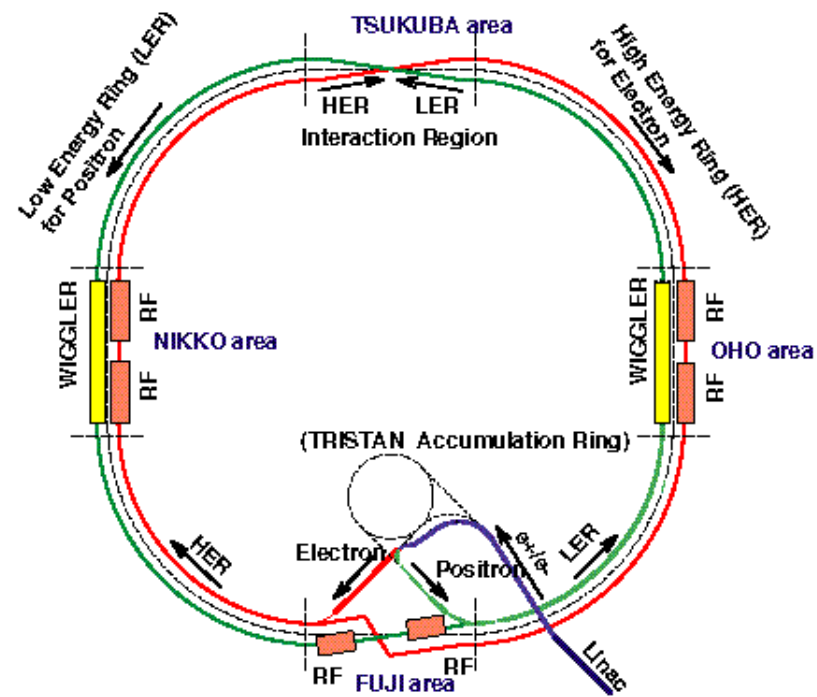
- KEKB ring and its status
- KEKB control system Status
- Operation statistics
- Upgrade/improvement
- Future Plans

▶ KEK-JAERI Joint Project (formally known as JHF)

▶ Training session in Shanghai

KEKB: what it is?

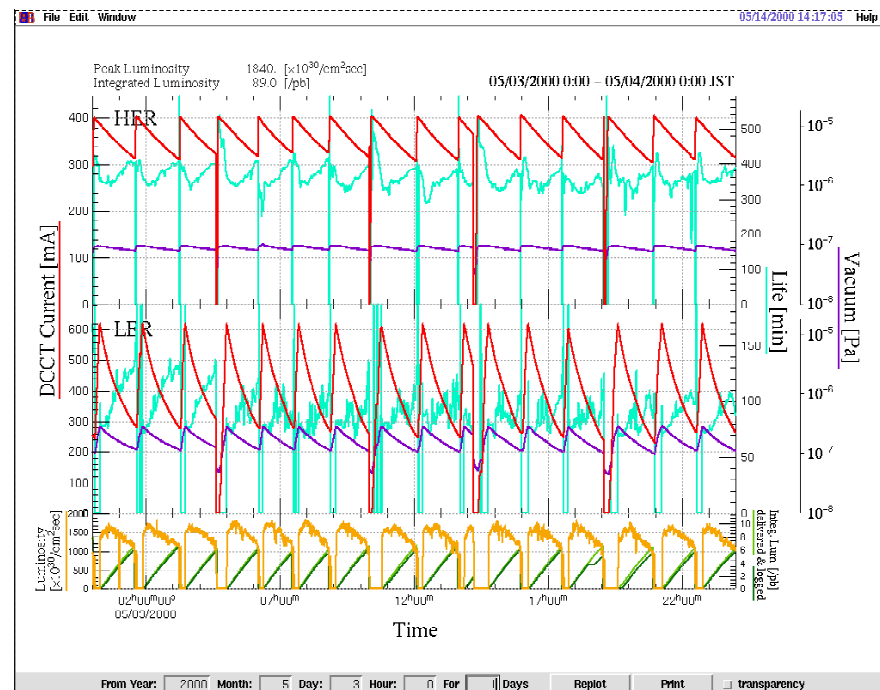
KEKB is an asymmetric electron-positron collider designed and running for B-meson physics.



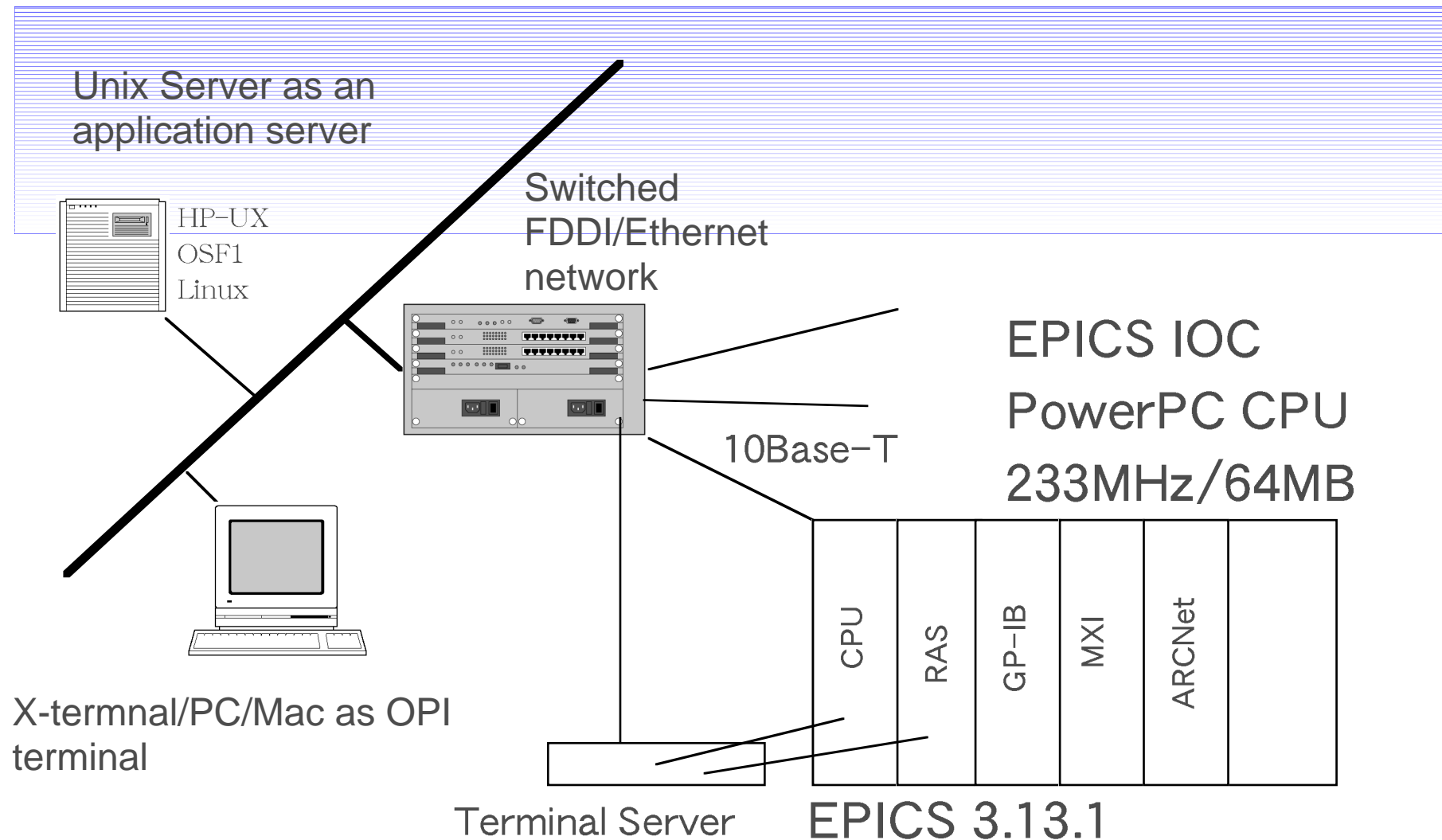
KEKB Status:

Running for the HE physics experiments

- Beam current 620mA(positron) x 400mA(electron)
- Peak Luminosity: 2×10^{33} 1/cm²/sec
- Luminosity per day: 89 1/pb



KEKB control system status



Statistics:IOC

EPICS Records

- Total 242,597 records on 94 IOCs [was 208,716 records on 90 IOCs]
- Max. 25,147 records on IOCMGD06
- Average 2,788.5 records [was 2,319]

Memory Usage on IOCs

- Allocated memory max. 48,149 KB (was 54,429 KB)
- Allocated memory min. 1,324 KB (was 1,309 KB)
- Allocated memory ave. 12,142 KB (4,594 KB)

EPICS related tools

▶ EPICS R3.13.1

- Base
- CAPFAST + e2db + dbLoadTemplate
- medm.2.4.x, dm2k
- StripTool/striptool
- AR_cmd
- probe

▶ ORACLE

▶ SAD/Tk

▶ Python/Tk

▶ Tcl/Tk

Statistics: Host Computers and Network

CPU load

- Load average 4-6 (was 15-20)
- Users 160
- Processes 900

Network Load

- around 2000 Packets/sec @ FDDI interface of a HP-UX host

Statistics: Software

Applications and Operator Display

	SAD	medm	python	misc	Total
In Top level Applications	141	74	42	6	263

Changes:HW

No Major change in HW

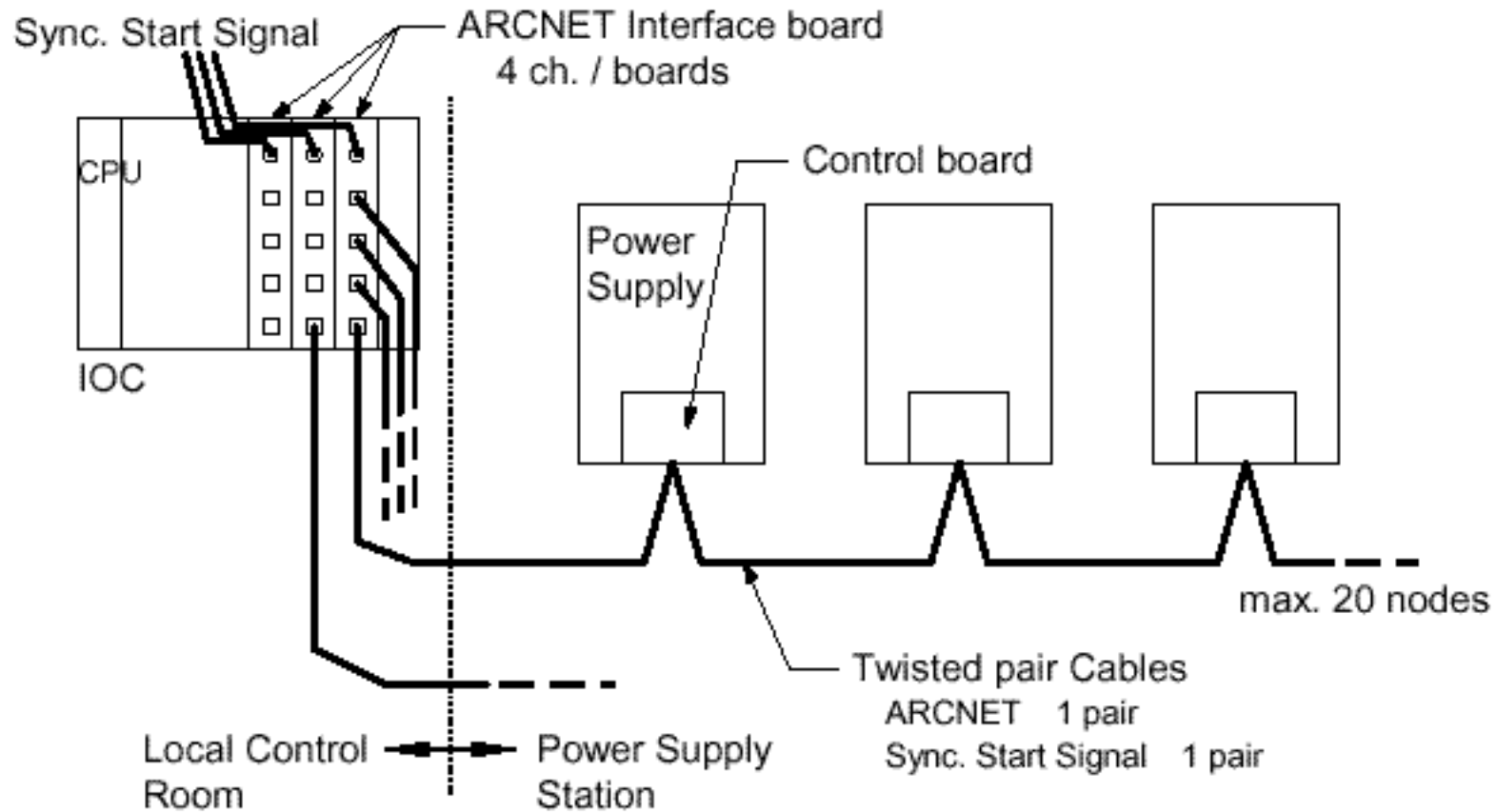
ARCNET HUB

- It enhanced stability of ARCNET operation(reduced number of automatic network reconfiguration)

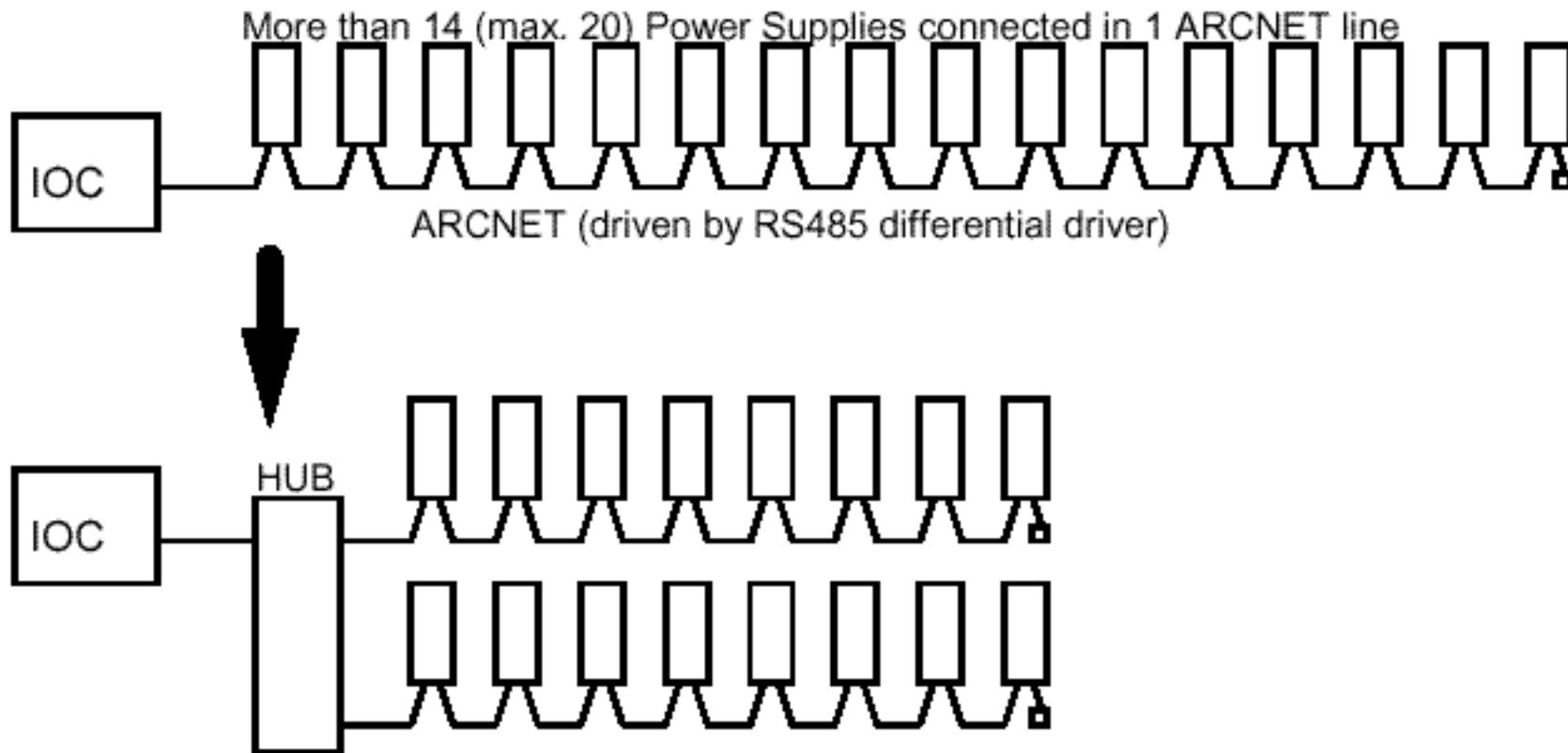
Additional WS and Storage for Operation

- Current WS in Control/Operation
 - ▶ 1 HP-UX WS
 - ORACLE
 - EPICS development
 - Operator interface
 - ▶ 3 Digital Unix WS and 3 Linux WS
 - Operator interface
 - Most optics related calculation

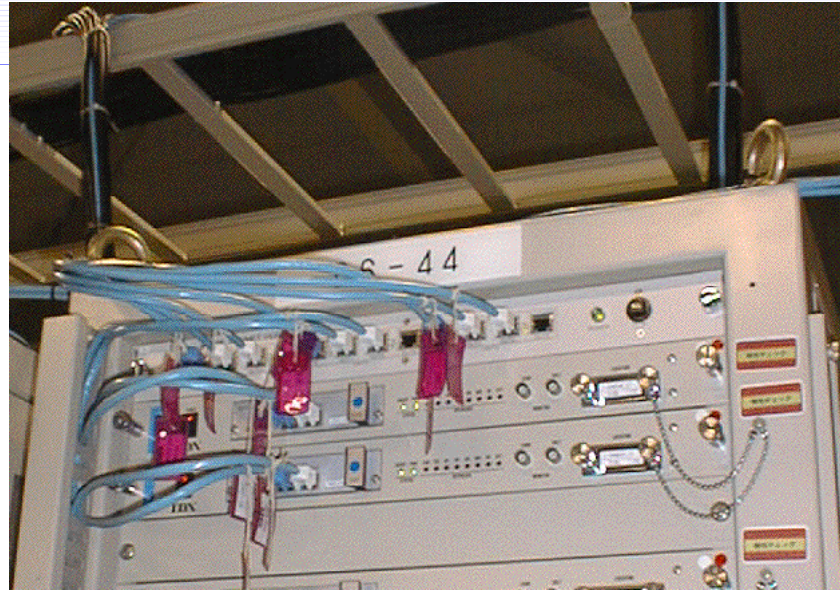
Configuration of Magnet Power Supply Control System



ARCNET HUB



ARCNET HUB @ Work



ARCNET --- Magnet Power Supply Control

ARCNET HUBs were introduced for more stable operation.

Installation of HUBs

- ▶ A “HUB Box” contains 3 HUBs
- ▶ 33 HUB Boxes were installed for 97 ARCNET lines
(mainly for the steering magnet power supplies)

Number of ARCNET reconfiguration (by lost token)

- ▶ Without HUBs : 10^2 times / day / 1 ARCNET line (worst case)
(ARCNET reconfiguration does not mean loss of communication.)
- ▶ With HUBs : No reconfiguration observed

Changes:HW

No Major change in HW

ARCNET HUB

Additional WS and Storage for Operation

- **Current WS in Control/Operation**

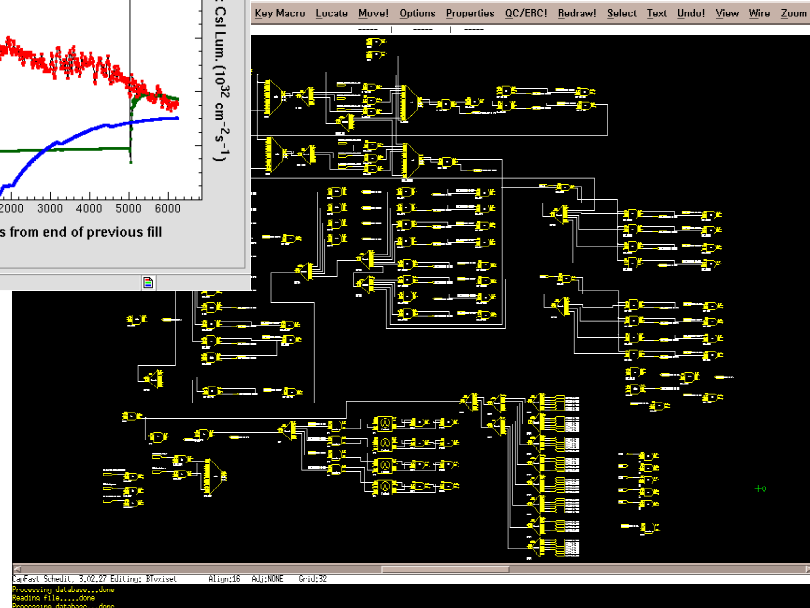
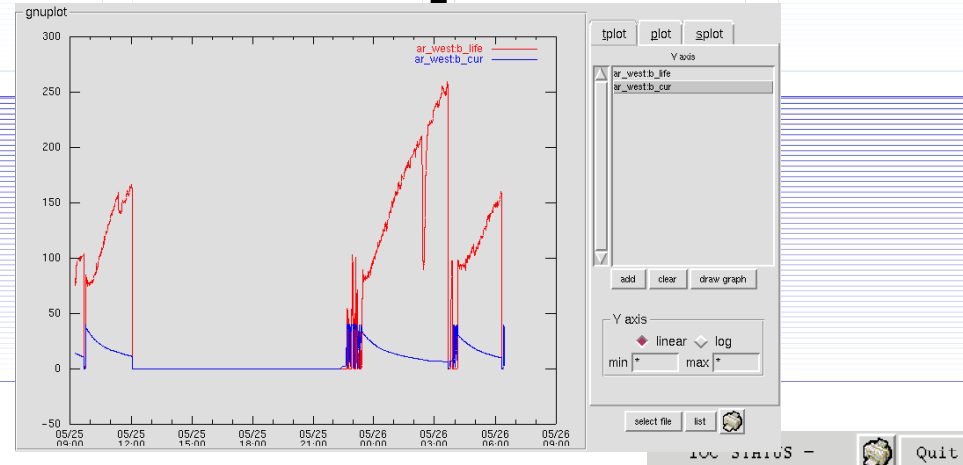
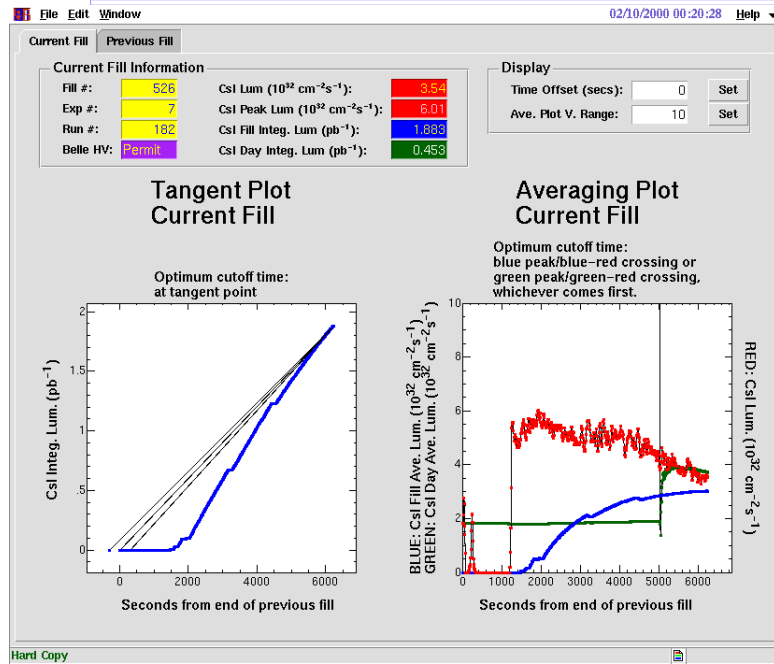
- ▶ **1 HP-UX WS**

- ORACLE
- EPICS development
- Operator interface

- ▶ **3 Digital Unix WS and 1 Linux WS**

- Operator interface
- Most optics related calculation

Examples of New/Improved Software



100 3inuS - Quit

Trouble has occurred!

BM	I	R	V	MG	I	R	V
BMCC0				MGCC0			
BM01				MG02			
BM02				MG03			
BM03				MG03B			
BM04				MG05			
BM05				MG06			
BM05B				MG08			
BM06				MG09			
BM07				MG09B			
BM08				MG11			
BM09				MG12			
BM10				MG12B			
BM11							
BM11B				RF			
BM12				RFCCC			
BMLC1				RF06			
BMLC2				RF07			
BMLC3				RF08			
BMLC4				RF08			
BMLC5				RF08			
BMLC6				RF08			
BMLC7				FB			
BMLC8				FB4B			
				FB4C			
				FB4D			
				FB4E			
				FB4F			
				FB4G			
				FB4H			
				FB4I			
				FB4J			
				FB4K			
				FB4L			
				FB4M			
				FB4N			
				FB4O			
				FB4P			
				FB4Q			
				FB4R			
				FB4S			
				FB4T			
				FB4U			
				FB4V			
				FB4W			
				FB4X			
				FB4Y			
				FB4Z			
BT							
BTBTA							
BTBTB							
BTARS							
BTARW							
VA							
VACBT							
VAD01							
VAD02							
VAD03							
VAD04							
VAD05							
VAD06							
VAD07							
VAD08							
VAD09							
VAD10							
VAD11							
VAD12							
CO							
COCCC							

FPGA on ARCNET driver board

Update FPGA program on ARCNET driver board

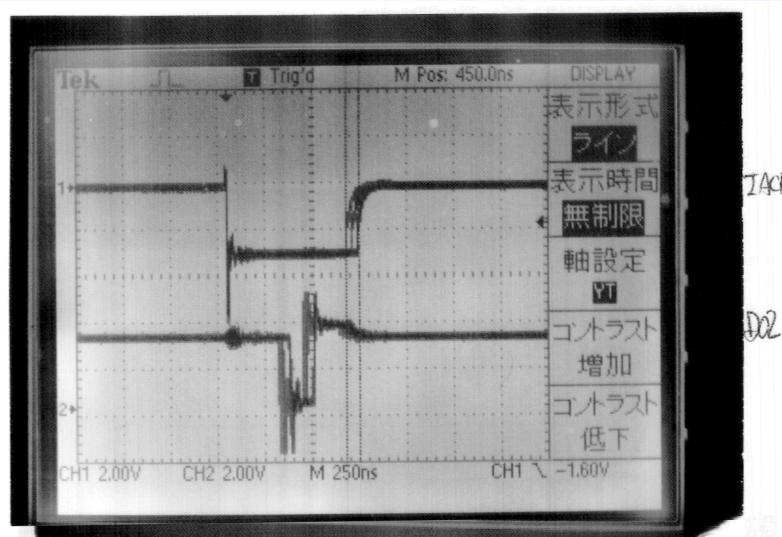
- The bug in the program prevents us from the operation of 8 ARCNET driver boards in the same IOC.
- Detailed analysis of signals on VME bus using an oscilloscope was needed to identify the source of the problem.

ARCNET driver: VME bus signal

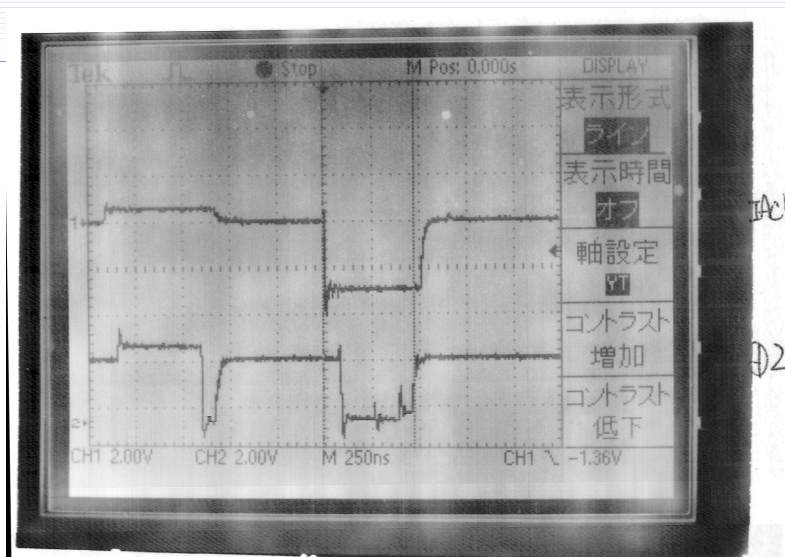
"Normal" Signal

IACK

D2



Abnormal Signal



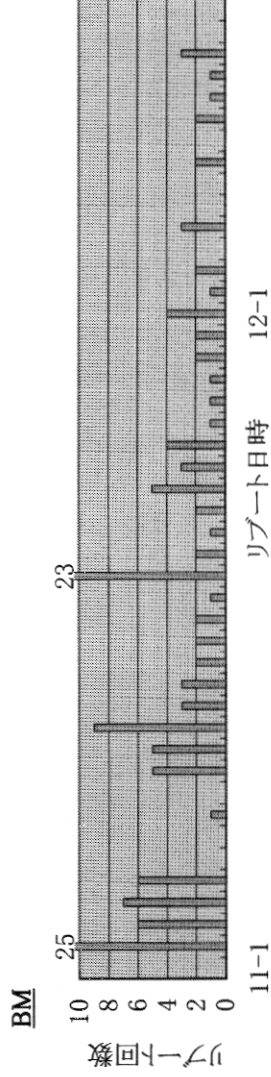
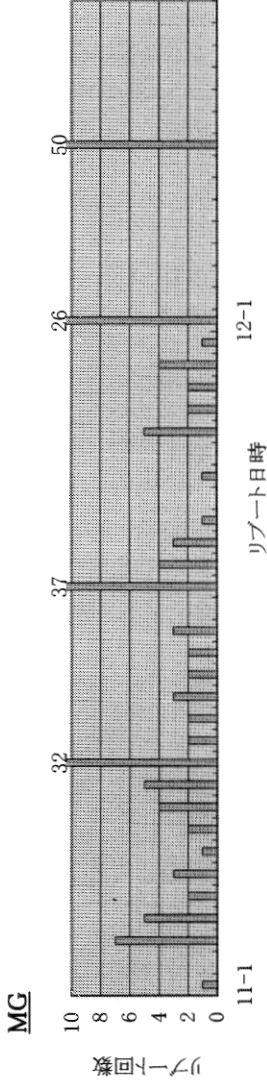
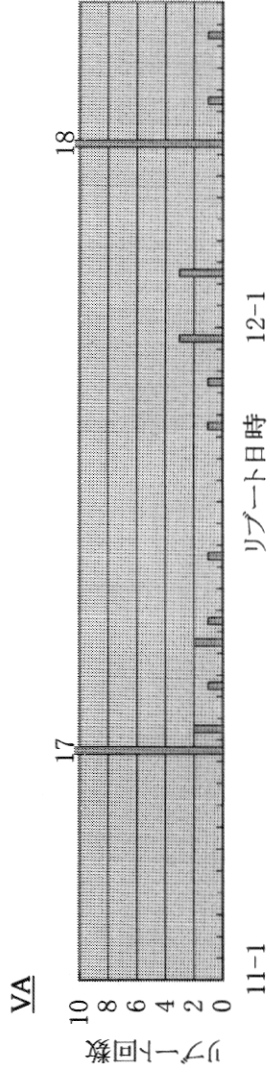
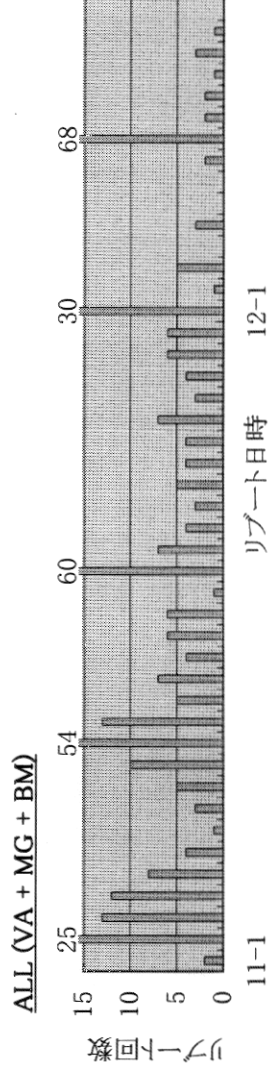
"Corrupt cmd in msg 5" in CA

In some situation, "Corrupt cmd in msg 5" appears on IOC log. When CA software outputs this message, it also consume small chunk of main memories on IOC and will not release it until IOC crashes.

It was found that one of network equipments responds to the CA name resolution(CA search) packet.

Solution of this is to reboot One particular Terminal server on the net.

IOC reboots



最近2ヶ月間のIOCリブート回数の変化 (1999.11.01 - 1999.12.15)

IOC status monitor

- ◆ Monitors a console port of IOC through Terminal server
- ◆ Monitors VxWorks status using devVXStats.c
- ◆ Timestamp record processed every second
- ◆ RAS board support

IOC STATUS | Quit

We are running

BM	I	R	V	MG	I	R	V
BMCCC	■	■	■	MGCCC	■	■	■
BMD01	■	■	■	MGD02	■	■	■
BMD02	■	■	■	MGD03	■	■	■
BMD03	■	■	■	MGD05	■	■	■
BMD04	■	■	■	MGD06	■	■	■
BMD05	■	■	■	MGD08	■	■	■
BMD05B	■	■	■	MGD09	■	■	■
BMD06	■	■	■	MGD11	■	■	■
BMD07	■	■	■	MGD12	■	■	■
BMD08	■	■	■				
BMD09	■	■	■	RF			
BMD10	■	■	■	RFCCC	■	■	■
BMD11	■	■	■	RFD04	■	■	■
BMD11B	■	■	■	RFD05	■	■	■
BMD12	■	■	■	RFD07	■	■	■
BMLC1	■	■	■	RFD08	■	■	■
BMLC2	■	■	■	RFD11	■	■	■
BMLC3	■	■	■				
BMLC4	■	■	■	FB			
BMLC5	■	■	■	FBFB4B	■	■	■
BMLC6	■	■	■	FBFB4C	■	■	■
BMLC7	■	■	■	FBFB4D	■	■	■
BMLC8	■	■	■	FBFB4F	■	■	■
				FBFB4G	■	■	■
				FBFB4H	■	■	■
				FBFB4I	■	■	■
BT	I	R	V				
BTCBTA	■	■	■				
BTCBTB	■	■	■				
BTARS	■	■	■				
BTARW	■	■	■				
VA	I	R	V				
VACBT	■	■	■				
VAD01	■	■	■				
VAD02	■	■	■				
VAD03	■	■	■				
VAD04	■	■	■				
VAD05	■	■	■				
VAD06	■	■	■				
VAD07	■	■	■				
VAD08	■	■	■				
VAD09	■	■	■				
VAD10	■	■	■				
VAD11	■	■	■				
VAD12	■	■	■				

BTCBTA
RAS Status
TEST : Normal
AUXPS: OK
AD : OK
+12V: OK
-12V: OK
FAN : OK
TEMP : OK

IOC STATUS | Quit

We are running

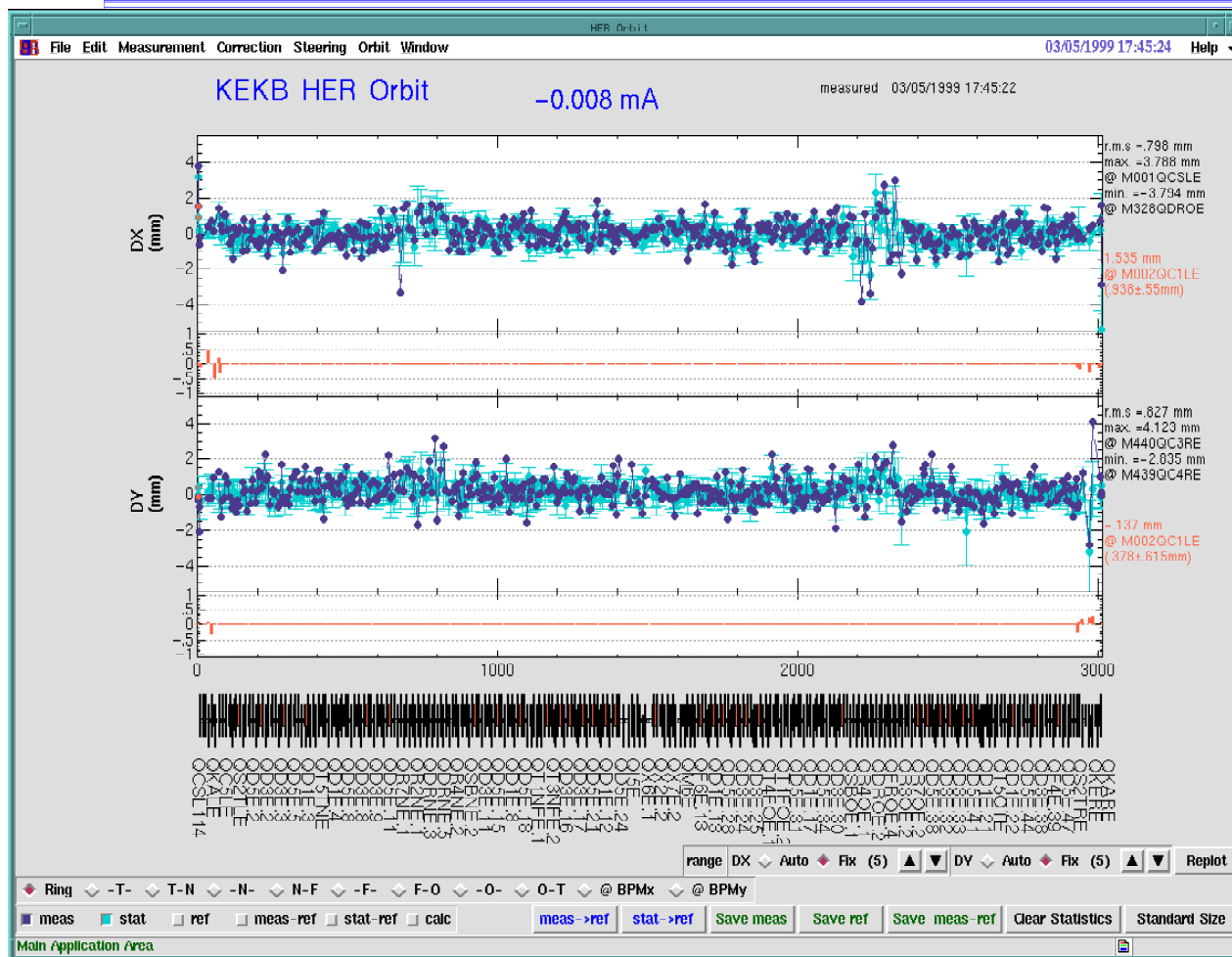
BM	I	R	V	MG	I	R	V
BMCCC	■	■	■	MGCCC	■	■	■
BMD01	■	■	■	MGD02	■	■	■
BMD02	■	■	■	MGD03	■	■	■
BMD03	■	■	■	MGD05	■	■	■
BMD04	■	■	■	MGD06	■	■	■
BMD05	■	■	■	MGD08	■	■	■
BMD05B	■	■	■	MGD09	■	■	■
BMD06	■	■	■	MGD11	■	■	■
BMD07	■	■	■	MGD12	■	■	■
BMD08	■	■	■				
BMD09	■	■	■	RF			
BMD10	■	■	■	RFCCC	■	■	■
BMD11	■	■	■	RFD04	■	■	■
BMD11B	■	■	■	RFD05	■	■	■
BMD12	■	■	■	RFD07	■	■	■
BMLC1	■	■	■	RFD08	■	■	■
BMLC2	■	■	■	RFD11	■	■	■
BMLC3	■	■	■				
BMLC4	■	■	■	FB			
BMLC5	■	■	■	FBFB4B	■	■	■
BMLC6	■	■	■	FBFB4C	■	■	■
BMLC7	■	■	■	FBFB4D	■	■	■
BMLC8	■	■	■	FBFB4F	■	■	■
				FBFB4G	■	■	■
				FBFB4H	■	■	■
				FBFB4I	■	■	■
				FBFB4J	■	■	■
				FBFB4	■	■	■
BT	I	R	V				
BTCBTA	■	■	■				
BTCBTB	■	■	■				
BTARS	■	■	■				
BTARW	■	■	■				
VA	I	R	V				
VACBT	■	■	■				
VAD01	■	■	■				
VAD02	■	■	■				
VAD03	■	■	■				
VAD04	■	■	■				
VAD05	■	■	■				
VAD06	■	■	■				
VAD07	■	■	■				
VAD08	■	■	■				
VAD09	■	■	■				
VAD10	■	■	■				
VAD11	■	■	■				
VAD12	■	■	■				

MGD03
VxWorks Status
free :19.78[M] 31.3% free.
alloc :43.5[M] 68.7% used.
load :11.11[%] OK
sockets:63.0 OK

Sync. operation of Magnet

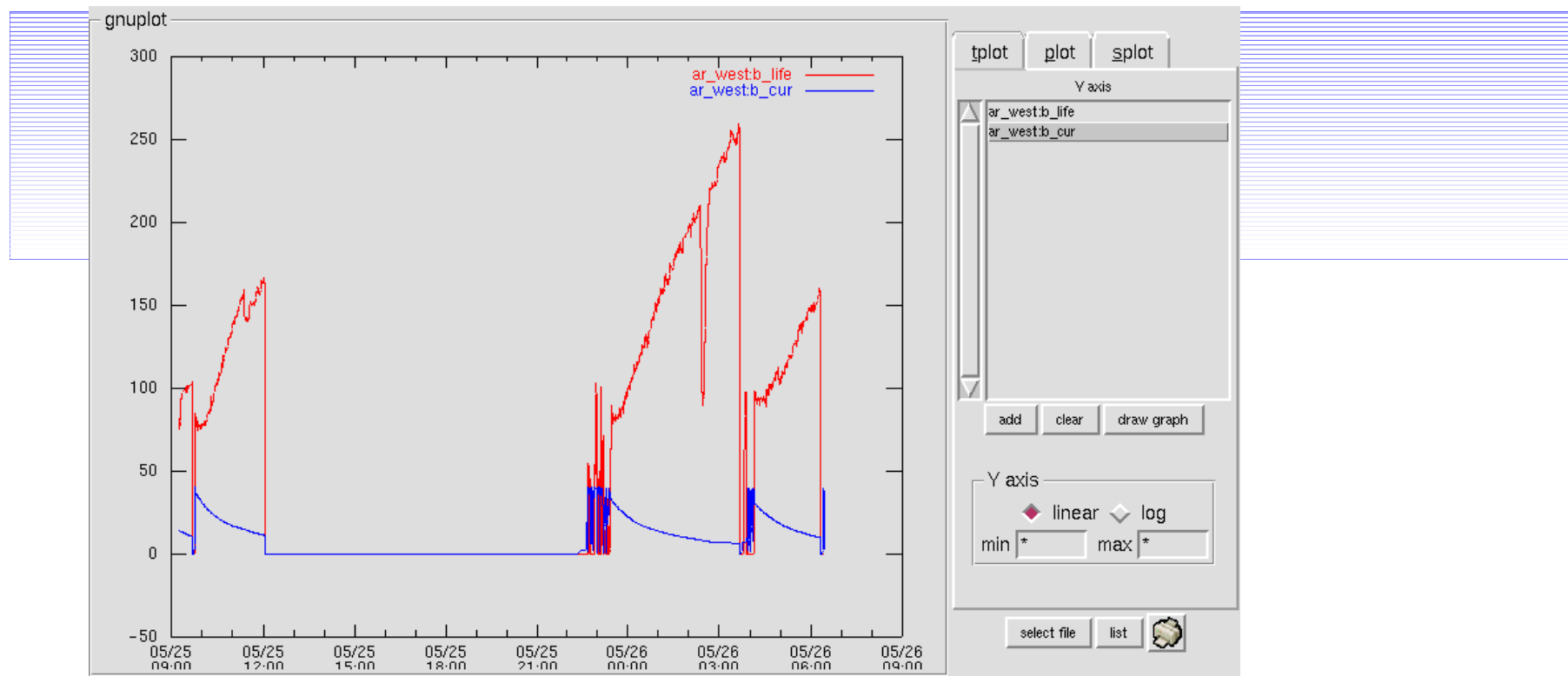
- ◆ Once ramp pattern is set to the each MG-PS controller, trigger pulse is sent to the PS to start PS ramp in synchronized way.
- ◆ Before setting the new value, the application(on WS) should get the semaphore for the modification of magnet setting and the trigger signal generation.
- ◆ Need Lock/Modify/commit/Unlock mechanism.
 - ◆ SyncSetServer (on Unix WS in Python + a few records on IOC)
 - ◆ Network Semaphore (on VxWorks)

SAD Application Example



Beam orbit
correction
application.
read BPM
readings and
calculate new
setting of orbit
correctors.

ARR.py/ ARR_plot.py



ARR.py/ ARR_plot.py

JAERI-KEK Joint project

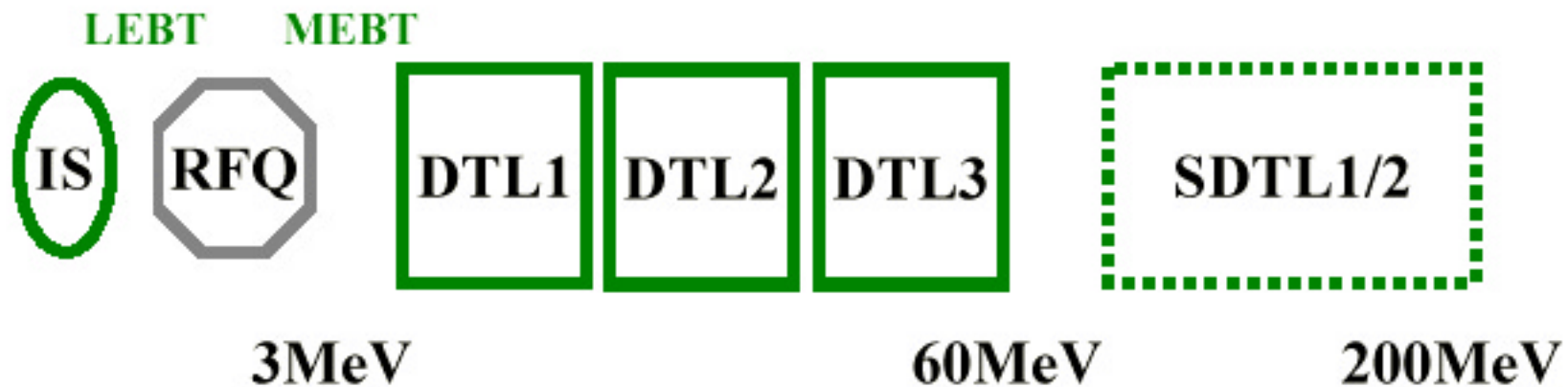
- ▶ KEK's JHF (Japane Hadron Facility) project and JAERI's NSP(Neutron Science Project)
- ▶ It is a High-Intensity Proton Accelerator.
- ▶ This project have not been approved yet
- ▶ 60-MeV LINAC is now under construction at the KEK site
- ▶ More info. on JAERI-KEK joint project can be found at
 - ▶ <http://www.jaeri.go.jp/english/press/990528/index.html>



60 MeV Linac (KEK)

JAERI-KEK
Joint Project
Control

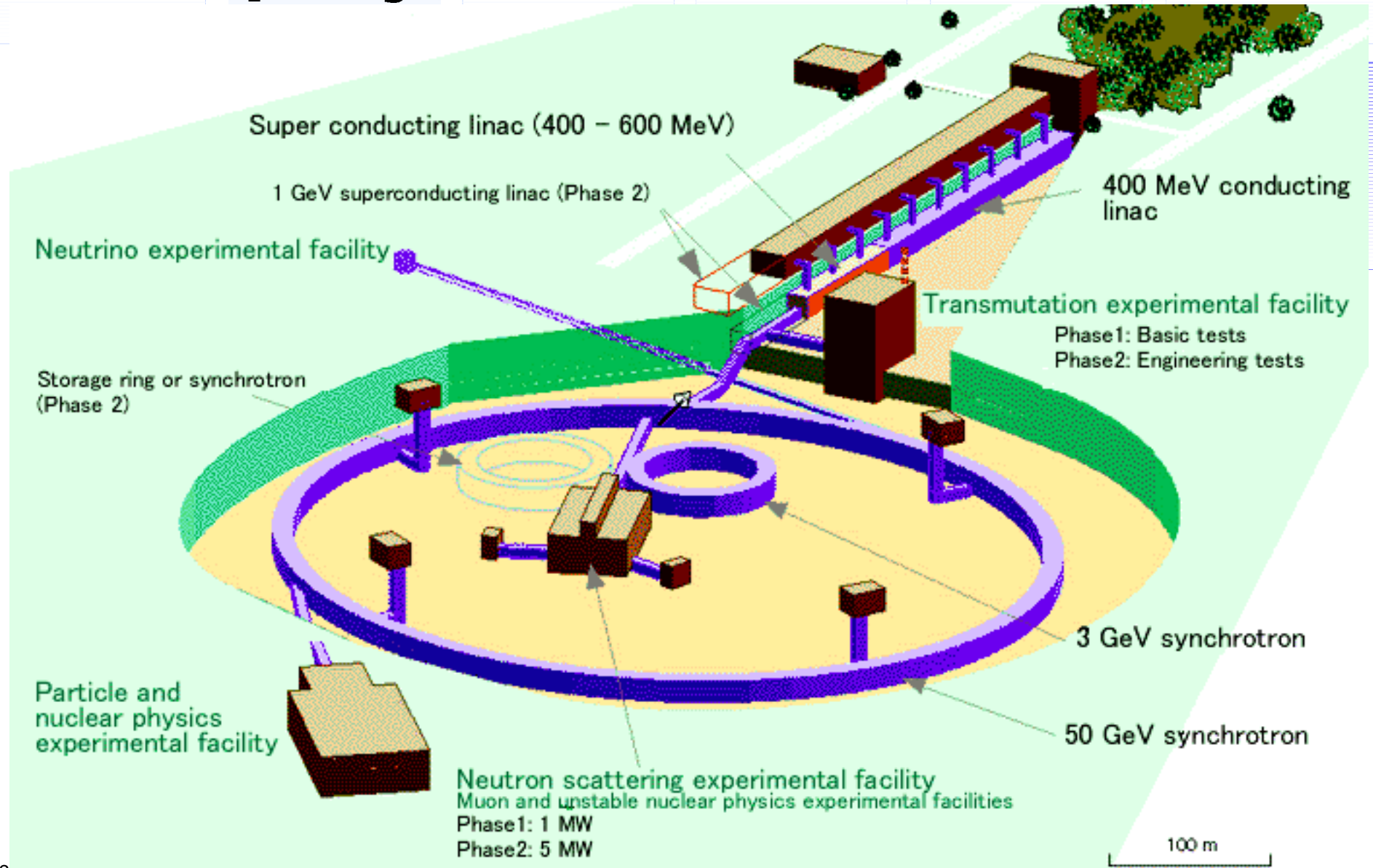
The construction of the
LINAC building was
completed in April, 2000.



May-17-2000

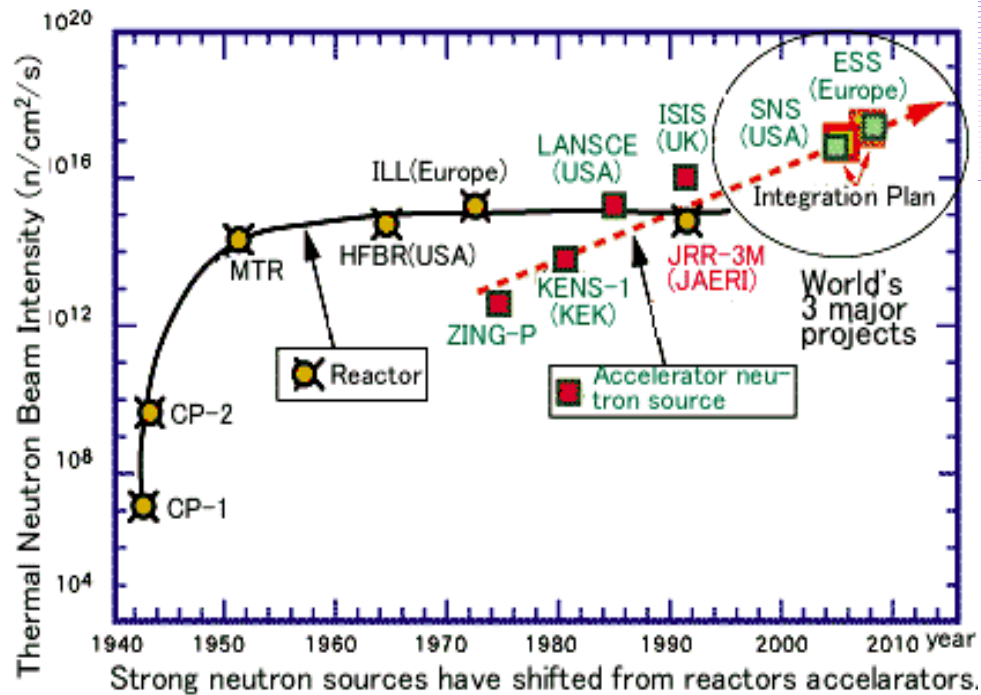
J.Chiba (KEK)

Joint project Accelerator Plan

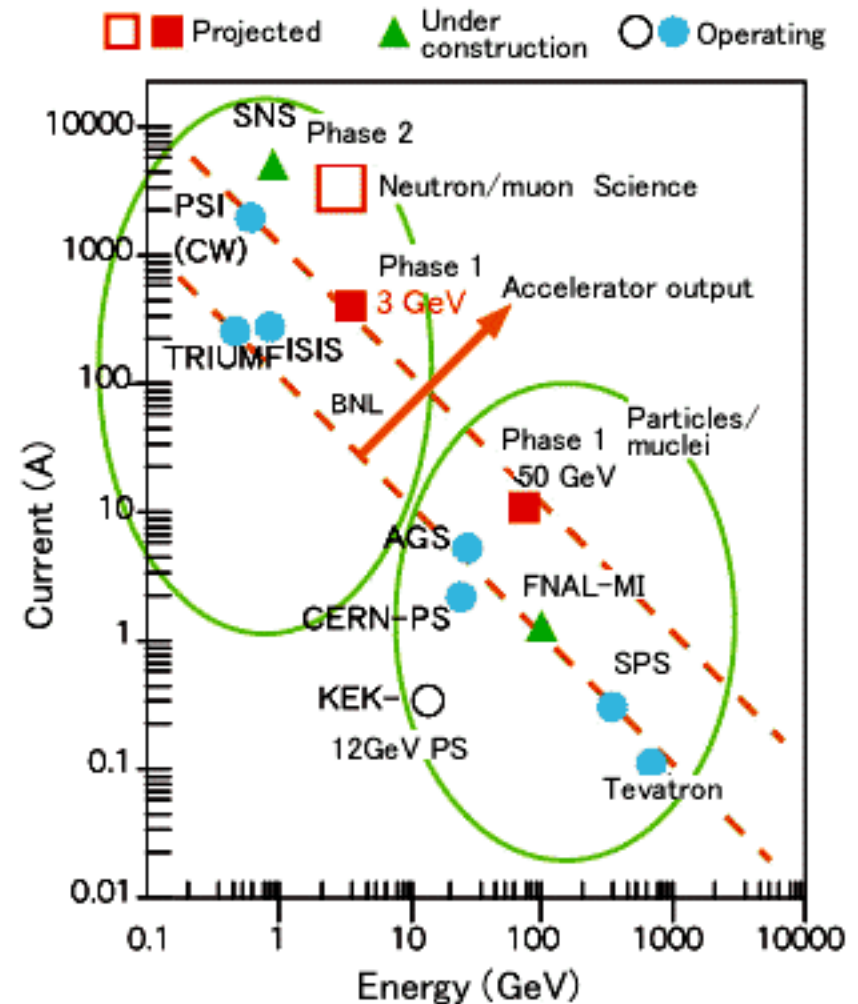


Present Status in the World

Development of Research-Purpose Neutron Sources



Major High Energy Proton Accelerators in the World



EPICS seminar in Shanghai



Participants

38 participants

- 30 from China (SSRC, NSRL and IHEP)
- 1 from Korea (POSTECH)
- 1 from Thailand (NSRC)
- 6 from JAPAN

Time Table

EPICS Seminar in Shanghai

	Monday, 8/28	Tuesday, 8/29	Wednesday, 8/30	Thursday, 8/31	Friday, 9/1	
9:00	on Accelerator Control systems. by T. Katoh	EPICS manager tasks (by N. Yamamoto)	Database Practice (Intro) by T. Nakamura	Sequencer and SNL by T. Nakamura	Portable CA Server by K. Furukawa	
	EPICS Overview (part-1) by N. Yamamoto					Database Practice
	Break					Break
10:30 -	EPICS Overview (part-2) by N. Yamamoto	High Level Application Development tools by N. Yamamoto		Sequencer and SNL Practice	Portable CA Server(cont.)	
11:45-13:00	Lunch	Lunch	Lunch	Lunch	Lunch	
13:00 -	EPICS system example:KEKB by T.Nakamura	EPICS Database by T. Namamura	CA Client Lecture by K. Furukawa	Record/Device/Driver Support (lecture) by J.-I. Odagiri	Channel Archiver by N. Yamamoto	
	Break	Break	Break	Break	Break	
14:45-	Display Manager Overview (incl. short practice) by N.Yamamoto	IOC application development/Debugging + Practice by J.-I. Odagiri	CA Client Practice	Record/Device/Driver Practice	Q/A	
16:30						