

# **IRMIS SIG Summary**

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A U.S. Department of Energy laboratory managed by The University of Chicago



#### **IRMIS PV**

- widely used tool to provide a 'global' view of the facility control software
- **PV** Datamining
  - basis for creating configuration files (archiever, alh, ...)
  - examine inter-IOC EPICS logic
  - CA-client crawlers

General community agreement on PV schema and crawler

How about for the hardware?

Configuration Control - capturing the control system hardware and connecting it with EPICS process variables

SLS - CIDB GANIL - carl\_master SLAC - captor

#### **DESY - deviceDB**





- Mature relational database schema
- Issues of management 'buy-in'
  - 'global' vs 'private' culture

## SNS - Wish List

- Standard prescriptive schema
- VDCT reads/writes to either ascii file or RDB
- Interface between EDM and RDB; maybe in CSS?

### **CLS: Process Flow Drawing**



Consolidation of the IRMIS 3-hierarchy model

> 30000 components have been installed in the APS IRMIS database including their control, housing and power relationships

- rigorous test of the component and component-type schema
- component-type definition refinements (esp interfaces)
- general acceptance by controls group

Primary (first line of defense) for controls group on-call

- operations usage 'master source'
- pressure to relate PVs to components (MEDM->hardware)

Datamining the component database

- ~600 control systems applications AOI
- CCMS control component montoring system

The IRMIS component-type model includes component ports - port-port connections form the basis of the cable database

Each port contains a set of pins

- each pin represents an input or output 'signal'

The 'signal' database ultimately relates an installed component to a set of software Process Variables

Integrated approach - the 'I' in IRMIS

- APS Control now complete
- IPNS
- BCDA
- IT

universal component type definition

➤associated device support



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# Work in Progress

-PV Info			Control	Housing
Record N	lame Type IQC	У		
L3:DG3:aOutputPo	ISetBO bo ioclic2	/	• MVME 5100-013x ioclibpm5	Room LINAC_Gallery_Area#1
	DB File(s)		MVME 167-box DBL ioclic1 MVME 167-box DBL ioclic2	Room LINAC_Gallery_Area#2
/net/helios/iocapps	/R31310/joc/linac/2/linacApp/timirgDb/dg1234567 dat		VME Chassis - System 22 Type 1-A	AC Panel ERP-J2
			- стм100 (стс100)_	- AC Panel ERP-J5
-DV Fields			9-1014D 0,1 0-GPIB Lank 0	P Rack L3:BC:RA:1
F V HGIU3			HP8648D 7	• Rack L3:BC:RA:3
Field 🔺	Value			🗣 Rack L3:CO:RA:1
DOL			OF DG53516	PRack L3:DU:RA:1
DTYP	DG535 Delay Generator (GP/IB)		0- DG935 17	Rack L3:DU:RA:2
EVNT	0			AC Panel L3:EL:SU1 Book 1 2:EU/(PA:1)
FLNK	L3:DG3:aOutputPoIBLVA		● DG535 20	Enclosure L3:IC1
	0		©- DG535 21	• Rack L3:10:RA:1
			🗣 DG535 22	🗣 Rack L3:10:RA:2
IVOA	Continue normally		● DG535 23	P Rack L3:10:RA:3
IVOV				
LALM				• BUG300 4
LONT			• 0008-SV 0	HP8648D 7
MASK			• 6008-SV 1	• DG53516
MLOT			• VMOD-20,1	P DG535 17
MLSI				
NAME		)	• FDM112	
NSEV			©- 7/M100_	- AB+
NSTA			DDPG02 0	- AB-
OMSL	supervisory		DDPG021	
ONAM			• DDPG02 3	- CD+
ORAW			• FOM102_	- CD-
ORBV	0	$\checkmark$	CTS100 0 CTS100 0	- Ext Trig - Trig Inhibit
OSV	NO_ALARM		• PPV100_	- TO (Back)
OUT	#L0 A17 @15		VME Power Supply - Type 1 _	A (Back)
PACT			Antimary Devices _	
Provide the second seco				

